

A0 046

S-DIAS Analog Output Module

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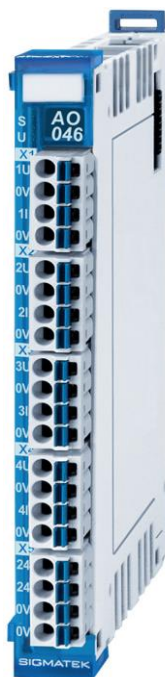
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S-DIAS Analog Output Module

AO 046

with 4 outputs (voltage/current switchable)

The S-DIAS AO 046 analog output module has four ± 10.8 V or 0-21.6 mA analog outputs with a resolution of 16 bits. The analog outputs are powered by an external +24 V supply. The analog output system is galvanically separated from the potential of the S-DIAS bus.



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1 Technical Data

1.1 Analog Output Voltage Specifications

Number of channels	4	
Output range	-10 ... +10 V	0 ... +10 V
Output value	-30,000 ... +30,000	0 ... +60,000
Output voltage over range	-10.8 ... +10.8 V	0 ... +10.8 V
Output value over range	-32,400 ... +32,400	0 ... +64,800
Resolution	16-bit (ca. 0.3 mV/LSB)	16-bit (ca. 0.15 mV/LSB)
Refresh time of all channels	minimum 100 µs (corresponds to the S-DIAS cycle time)	
Output voltage capacity	maximum 2 mA	
Allowable capacitive load	maximum 100 nF	
Short circuit protection	yes	
Settling time	200 µs typical for C < 100 nF (99.9 % of the end value)	
Galvanic isolation	yes (500 V) ⁽¹⁾	
Output precision	±0.04 % of maximum output value	

1.2 Output Accuracy Analog Outputs Voltage

Basic accuracy incl. calibration error and noise 25 °C	0.015 %
--	---------

Temperature drift 20-40 °C	0.008 %
0-55 °C	0.015 %
Linearity	0.010 %
Crosstalk	< 0.001 %
Total error 20-40 °C	±0.035 % (±3.5 mV)
0-55 °C	±0.045 % (±4.5 mV)

Tolerances caused by aging are not taken into consideration. A calibration is necessary at the latest after 12 months.

1.3 Analog Output Current Specifications

Number of channels	4	
Output range	0-20 mA	4-20 mA
Output value	0-60,000	12,000-60,000
Output voltage over range	0-21.6 mA	3.8-21.6 mA
Output value over range	0-64,800	11,400-64,800
Resolution current	16-bit (ca. 0.3 μ A/LSB)	
Refresh time of all channels	minimum 100 μ s (corresponds to the S-DIAS cycle time)	
Settling time	200 μ s typically for L < 0.5 mH at 50 Ω 200 μ s typically for L < 5 mH at 500 Ω	
Load	maximum 500 Ω	
Allowed output inductivity	maximum 0.5 mH at 50 Ω maximum 5 mH at 500 Ω	
Cable break monitor	yes	
Galvanic isolation	yes (500 V) ⁽¹⁾	
Output precision	\pm 0.17 % of maximum output value	

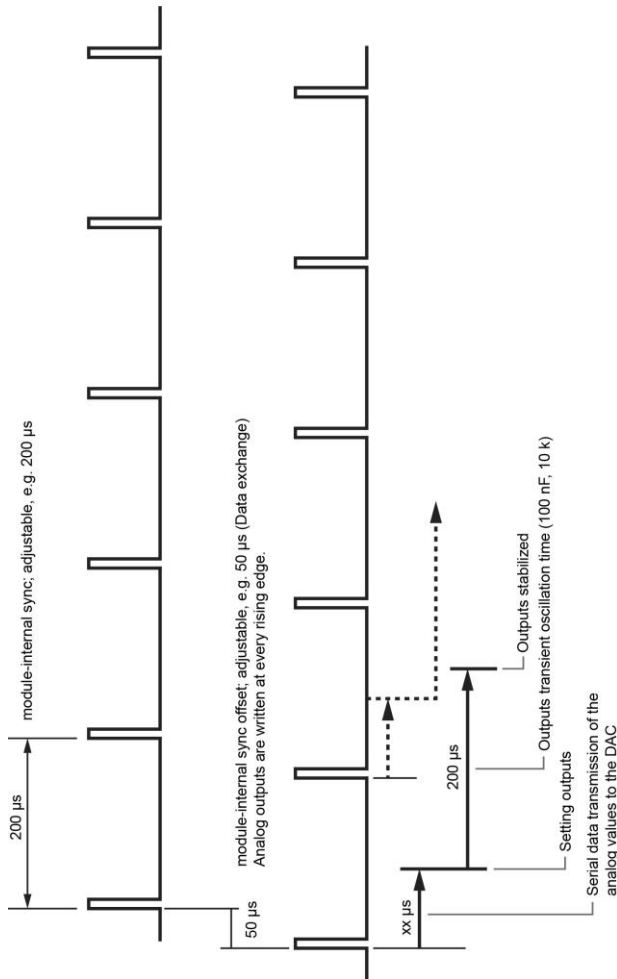
⁽¹⁾ The galvanic separation exists between the S-DIAS bus and combined analog outputs.

1.4 Output Accuracy Analog Outputs Current

Basic accuracy incl. calibration error and noise 25 °C	0.045 %
Temperature drift	
20-40 °C	0.028 %
0-55 °C	0.055 %
Linearity	0.035 %
Crosstalk	< 0.001 %
Total error	
20-40 °C	\pm 0.140 % (\pm 28 μ A)
0-55 °C	\pm 0.170 % (\pm 34 μ A)

Tolerances caused by aging are not taken into consideration. A calibration is necessary at the latest after 12 months.

1.5 Settling Time of the Analog Outputs



The diagram should display the settling time curve of the analog outputs. An internal module offset for the time of the S-DIAS Sync can be set. Starting from this time point, the constant write process from the S-DIAS bus in the direction of the digital-analog converter begins. At the end of this time, the new analog values of all channels are switched from the digital-analog converter to the converter output. From this point, the settling time of 200 µs starts in all channels. At the end of this time, all analog inputs are stabilized to the new output value.

1.6 Electrical Requirements

External +24 V supply	+18-30 V DC	
Current consumption of the +24 V supply without load on the analog outputs	typically 34 mA	maximum 44 mA
Current consumption of the +24 V supply with a load on the analog outputs per 20 mA	typically 92 mA	maximum 122 mA
Voltage supply from S-DIAS bus	+5 V	
Current consumption on the S-DIAS bus (+5 V supply)	0	0
Voltage supply from S-DIAS bus	+24 V	
Current consumption on the S-DIAS bus (+24 V supply)	typically 22 mA	maximum 26 mA

If this S-DIAS module is connected to an S-DIAS supply module with several S-DIAS modules, the total current of the modules used must be determined and checked.

The total current of the +24 V supply cannot exceed 1.6 A!

The total current of the +5 V supply cannot exceed 1.6 A!

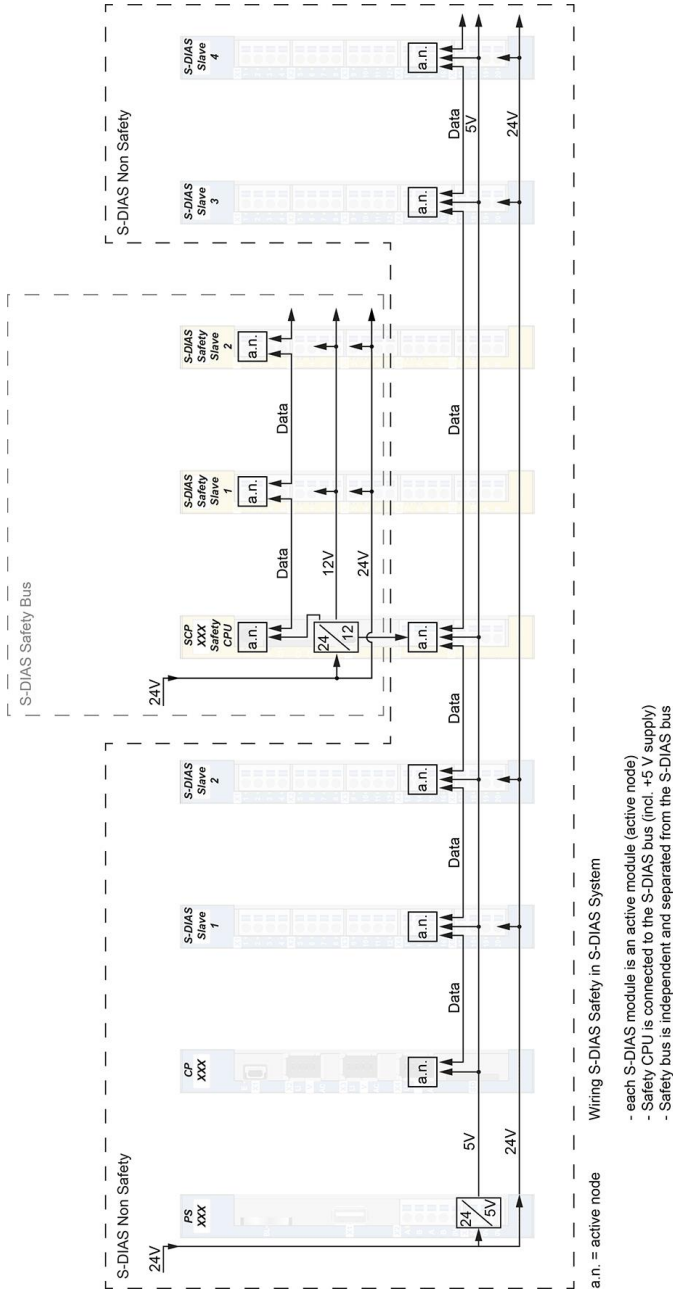
The specification for the current can be found in the module-specific documentation under "Electrical Requirements".

Si ce module S-DIAS est connecté à un module d'alimentation S-DIAS suivi de plusieurs modules S-DIAS, le courant total des modules utilisés doit être déterminé et vérifié.

Le courant total de l'alimentation +24 V ne peut pas dépasser 1,6 A!

Le courant total de l'alimentation +5 V ne peut pas dépasser 1,6 A!

Le cahier des charges pour le courant peut être trouvé dans la documentation spécifique au module sous "Spécifications électriques".



1.7 Voltage Monitoring External +24 V Supply

Supply voltage +24 V	query of the supply voltage with hysteresis: voltage > 18,0 V -> LED on, ExternVoltageOk=1 voltage < 16,0 V -> LED off, ExternVoltageOk=0 (DC OK-LED shines green)
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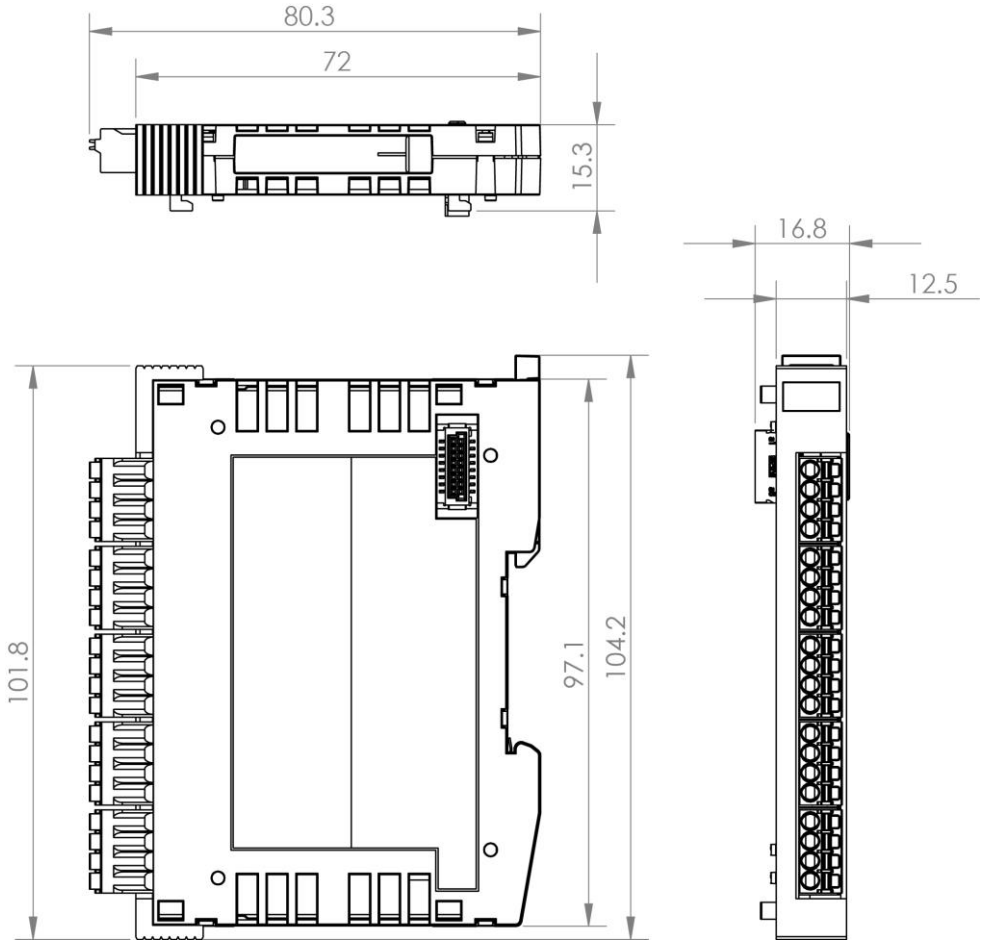
1.8 Miscellaneous

Article number	20-010-046
Hardware version	1.x
Standard	UL (in preparation)

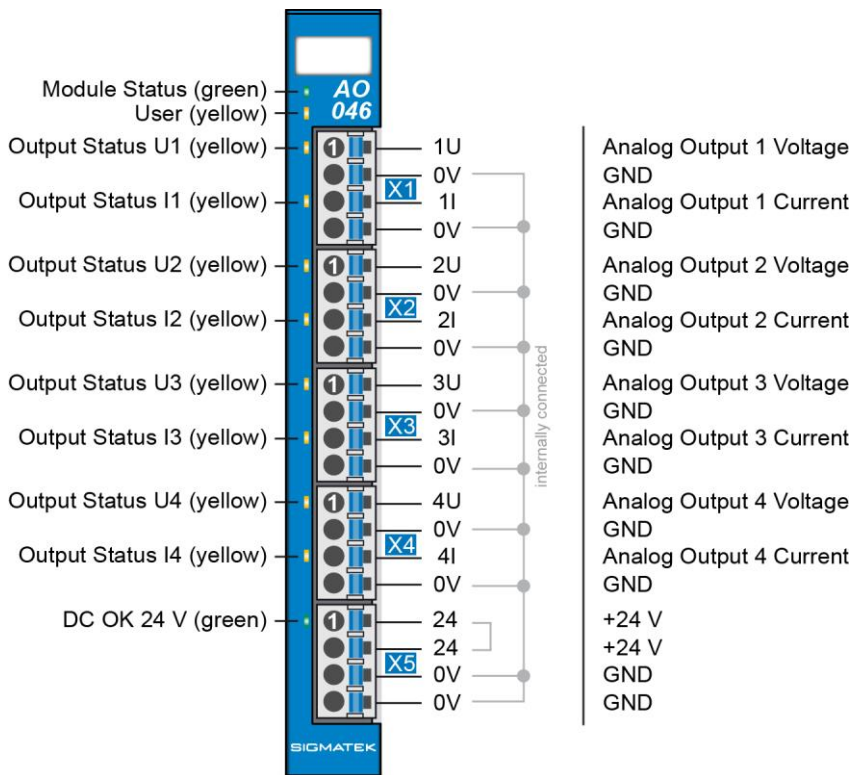
1.9 Environmental Conditions

Storage temperature	-20 ... +85 °C	
Environmental temperature	0 ... +55 °C	
Humidity	0-95 %, non-condensing	
Operating conditions	Pollution degree 2 altitude up to 2000 m	
EMC resistance	in accordance with EN 61000-6-2 (industrial area)	
EMC noise generation	in accordance with EN 61000-6-4 (industrial area)	
Vibration resistance	EN 60068-2-6	3.5 mm from 5-8.4 Hz 1 g from 8.4-150 Hz
Shock resistance	EN 60068-2-27	15 g
Protection type	EN 60529	IP20

2 Mechanical Dimensions



3 Connector Layout



IMPORTANT:
 Per channel, either voltage or current can be output only!
 Between the channels, it possible to select between current and voltage as desired!

IMPORTANT :
 Le canal de sortie n'admet qu'une sortie tension ou une sortie courant.
 Le choix de type de sortie est libre pour chaque canal.

The connections of the +24 V supply (X5: pin 1 and pin 2) or the GND supply (X5: pin 3 and pin 4) are internally bridged. To supply the module, only one connection to a +24 V pin (pin 1 or pin 2) and a GND pin (pin 3 or pin 4) is required. The bridged connections may be used for further looping of the +24 V supply and the GND supply. However, it must be taken into account that a total current of 6 A per connection is not exceeded by the forward looping!

3.1 Status LEDs

Module Status	green	ON	module active
		OFF	No supply available
		BLINKING (5 Hz)	no communication
User	yellow	ON	can be set from the application (e.g. the module LED can be set to blinking through the visualization so that the module is easily found in the control cabinet)
		OFF	
		BLINKING (2 Hz)	
		BLINKING (4 Hz)	
Output status Ux	yellow	ON	Analog output voltage active
		BLINKS	Analog output voltage error
		OFF	Analog output voltage inactive
Output status Ix	yellow	ON	Analog output current active
		BLINKS	Analog output current error
		OFF	Analog output current inactive
		USER	LED can be optionally controlled via the application
DC OK	green	ON	+24 V supply provided for analog output
		OFF	No supply voltage available

3.2 Applicable Connectors

Connectors:

X1, X2: Connectors with spring terminals (included in delivery)

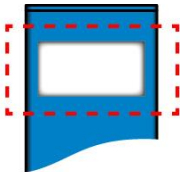
The spring terminals are suitable connecting ultrasonically compacted (ultrasonically welded) strands.

Connections:

Stripping length/Sleeve length:	10 mm
Mating direction:	parallel to the conductor axis or circuit board
Conductor cross section rigid:	0.2-1.5 mm ²
Conductor cross section flexible:	0.2-1.5 mm ²
Conductor cross section ultrasonically compacted:	0.2-1.5 mm ²
Conductor cross section AWG/kcmil:	24-16
Conductor cross section flexible with ferrule without plastic sleeve:	0.25-1.5 mm ²
Conductor cross section flexible with ferrule with plastic sleeve:	0.25-0.75 mm ² (reason for reduction d2 of the ferrule)



3.3 Label Field



Manufacturer	Weidmüller
Type	MF 10/5 CABUR MC NE WS
Weidmüller article number	1854510000
Compatible printer	Weidmüller
Type	Printjet Advanced 230V
Weidmüller article number	1324380000

4.2 Note

The signals recorded by the analog modules are very small, as compared to the digital signals. To ensure error-free operation, a careful wiring method must be followed:

- The 0 V connection of the supply voltage must be connected with the 0 V collection point over the shortest route possible.
- The DIN rail must have an adequate mass connection.
- The lines connected to the source of the analog signals must be as short as possible and parallel wiring to digital signal lines must be avoided.
- The signal lines must be shielded.
- The shielding must be connected to a shielding bus.
- Protective circuits for all relays (RC networks or free-wheeling diodes)
- Correct wiring to mass

The ground bus should be connected to the control cabinet when possible!

Si possible la terre doit être connectée à l'armoire de commande!

IMPORTANT:

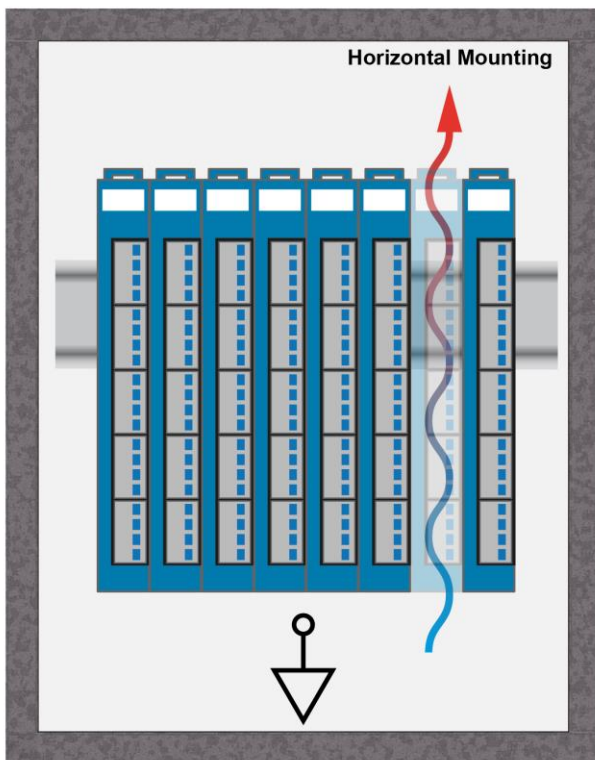
The S-DIAS module CANNOT be connected or disconnected while voltage is applied!

IMPORTANT:

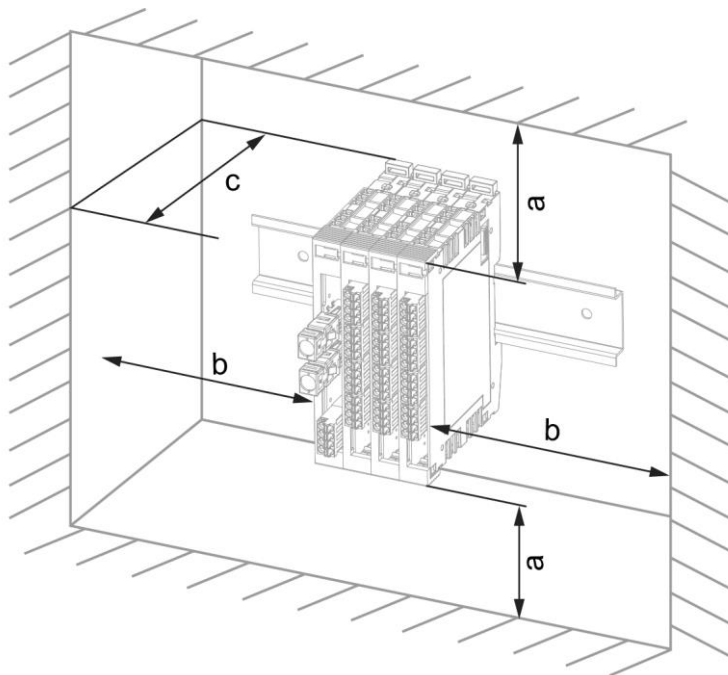
Le module S-Dias NE PEUT PAS être inséré ou retiré sous tension.

5 Mounting

The S-DIAS modules are designed for installation into the control cabinet. To mount the modules a DIN-rail is required. The DIN rail must establish a conductive connection with the back wall of the control cabinet. The individual S-DIAS modules are mounted on the DIN rail as a block and secured with latches. The functional ground connection from the module to the DIN rail is made via the grounding clamp on the back of the S-DIAS modules. The modules must be mounted horizontally (module label up) with sufficient clearance between the ventilation slots of the S-DIAS module blocks and nearby components and/or the control cabinet wall. This is necessary for optimal cooling and air circulation, so that proper function up to the maximum operating temperature is ensured.



Recommended minimum distances of the S-DIAS modules to the surrounding components or control cabinet wall:



a	b	c
30 mm (1.18")	30 mm (1.18")	100 mm (3.94")

a, b, c ... distances in mm (inches)

6 Addressing

Address (hex)	Size (bytes)	Access Type	Description	Reset value
000E	18	r/w	PDO Write	
000E	2	w16	LED Overwrite Register Bit 1...0: LED Channel 1 00 = off (HW status active, no SW overwrite) 01 = Enable SW Overwrite "0" 11 = Enable SW Overwrite "1" 10 = Reserved Bit 3...2: LED Channel 2 Bit 5...4: LED Channel 3 Bit 7...6: LED Channel 4 Bit 15..8: Reserved	0000
0010	2	w16	Analog output 1	0000
0012	2	w16	Analog output 2	0000
0014	2	w16	Analog output 3	0000
0016	2	w16	Analog output 4	0000
0018	2	w16	Offset time of the output in the output 1 cycle [1 µs]	0000
001A	2	w16	Offset time of the output in the output 2 cycle [1 µs]	0000
001C	2	w16	Offset time of the output in the output 3 cycle [1 µs]	0000
001E	2	w16	Offset time of the output in the output 4 cycle [1 µs]	0000
0020	16	r/w	Reserved	00

0030	2	r/w	PDO Read	
0030	2	r	Status Register Bit 0: edge limit analog output 1 Bit 1: edge limit analog output 2 Bit 2: edge limit analog output 3 Bit 3: edge limit analog output 4 Bit 4: temperature > 142 °C analog output 1 Bit 5: temperature > 142 °C analog output 2 Bit 6: temperature > 142 °C analog output 3 Bit 7: temperature > 142 °C analog output 4 Bit 8: over current analog output 1 (voltage) analog output 1 (current) voltage too high Bit 9: over current analog output 2 (voltage) analog output 2 (current) voltage too high Bit 10: over current analog output 3 (voltage) analog output 3 (current) voltage too high Bit 11: over current analog output 4 (voltage) analog output 4 (current) voltage too high Bit 12: calibration data could not be read Bit 13: calibration data invalid (CRC error) Bit 14: DC OK on DAC Bit 15: offset times must be corrected (deleted when read)	0000
0032	14	r/w	Reserved	00

0040	11	r/w	SDO	
0040	2	w64	Raw value output 1 (for PMB)	0000
0042	2		Raw value output 2 (for PMB)	0000
0044	2		Raw value output 3 (for PMB)	0000
0046	2		Raw value output 4 (for PMB) ⁽¹⁾	0000
0048	2	r/w16	DAC Control Bit 0: PMB mode Bit 1: enable offset time Bit 3...2: Reserved Bit 4: DAC SW reset analog output 1 Bit 5: DAC SW reset analog output 2 Bit 6: DAC SW reset analog output 3 Bit 7: DAC SW reset analog output 4 Bit 8: output 1 (0 = voltage, 1 = current) Bit 9: Output 2 (0 = voltage, 1 = current) Bit 10: output 1 (0 = voltage, 1 = current) Bit 11: output 1 (0 = voltage, 1 = current) Bit 12: mode output 1 0 = voltage -10 V to +10 V or 0 = current 0 mA to 24 mA 1 = voltage 0 V to +10 V or 1 = current 4 mA to 20 mA Bit 13: mode output 2 Bit 14: mode output 3 Bit 15: mode output 4	00F0
004A	1	w	Microblaze Control Bit 4...0: Reserved Bit 5: μ C Operational (read only) Bit 6: Reload configuration (1 = Tick to MicroBlaze, irq_config) Bit 7: MicroBlaze (μ C) Reset (1 = MicroBlaze in Reset)	80
004B	693	r/w	Reserved	-
0300	256	r/w	Calibration for calibration data	-

⁽¹⁾ Note: If written to this register, the values are immediately sent to the DAC

7 Hardware Class AO046

Hardware Class AO046 For the S-DIAS AO046 analog output module

```

SDIAS:20, AO046 (A00461)
[S] Class State (ClassState) <-[]->
[S] Device ID (DeviceID) <-[]->
[S] FPGA Version (FPGAVersion) <-[]->
[S] Hardware Version (HwVersion) <-[]->
[S] Serial Number (SerialNo) <-[]->
[S] Retry Counter (RetryCounter) <-[]->
[O] LED Control (LEDControl) <-[]->
[S] Error Bits (ErrorBits) <-[]->
[S] Extern Voltage Ok (ExternVoltageOk) <-[]->
[S] Time Offset Error Bits (TimeOffsetErrorBits) <-[]->
----- Analog Output 1 -----
[O] Analog Output 1 (A01) <-[]->
[O] Time Offset Analog Output 1 (A01TimeOffset) <-[]->
[O] Set Led Analog Output 1 (A01SetLed) <-[]->
----- Analog Output 2 -----
[O] Analog Output 2 (A02) <-[]->
[O] Time Offset Analog Output 2 (A02TimeOffset) <-[]->
[O] Set Led Analog Output 2 (A02SetLed) <-[]->
----- Analog Output 3 -----
[O] Analog Output 3 (A03) <-[]->
[O] Time Offset Analog Output 3 (A03TimeOffset) <-[]->
[O] Set Led Analog Output 3 (A03SetLed) <-[]->
----- Analog Output 4 -----
[O] Analog Output 4 (A04) <-[]->
[O] Time Offset Analog Output 4 (A04TimeOffset) <-[]->
[O] Set Led Analog Output 4 (A04SetLed) <-[]->
[ALARM:00, Empty]

```

This hardware class is used to control the AO 046 hardware module. The module has 4 (± 10 V, 0-10 V or 0-20 mA, 4-20 mA) analog outputs. More information on the hardware can be found in the module documentation.

7.1 General

Class state	State	This server shows the actual status of the hardware class.								
Device ID	State	The device ID of the hardware module is shown in this server.								
FPGA version	State	FPGA version of the module in 16#XY (e.g. 16#10 = version 1.0).								
Hardware version	State	Hardware version of the module in format 16#XXYY (e.g. 16#0120 = Version 1.20)								
Serial Number	State	The serial number of the hardware module is shown in this server.								
Retry counter	State	This server increments when a transfer fails.								
LED control	Output	<p>With this server, the application LED of the S-DIAS module can be activated to find the module in the network more quickly. The following statuses are possible:</p> <table border="1" data-bbox="380 491 991 619"> <tr> <td>0</td> <td>LED off</td> </tr> <tr> <td>1</td> <td>LED on</td> </tr> <tr> <td>2</td> <td>blinks slowly</td> </tr> <tr> <td>3</td> <td>blinks rapidly</td> </tr> </table>	0	LED off	1	LED on	2	blinks slowly	3	blinks rapidly
0	LED off									
1	LED on									
2	blinks slowly									
3	blinks rapidly									
Required	Property	This client is active by default, which means that the S-DIAS hardware module at this position is mandatory for the system and can under no circumstances be disconnected or return an error. Otherwise, the entire hardware deactivated. If the hardware module is missing or removed, an S-DIAS error is triggered. If his client is initialized with 0, the hardware module located in this position is not mandatory. This means that it can be inserted or removed at any time. However, which components identified as "not required" should be selected with regard to the safety of the system.								

7.2 Analog Outputs [1-4]

Analog Output [1-4]	Output	The analog output can be set with the write() method. Values are output depending on configuration. With the "Analog Output Config" 4-7, the output value can be overridden with the overrange settings. 8% for the voltage and +8%/-1% for the current output
Analog Output [1-4] Channel Active	Property	Here, the analog output can be activated/deactivated. Possible values: 0 channel deactivated 1 channel activated as initialization value
Analog Output [1-4] Config	Property	The desired analog output type and its range must be selected here as Init value. Possible values: 0 analog output -10 ... +10 V 1 analog output 0 ... +10 V 2 analog output 0 ... +20 mA 3 analog output +4 ... +20 mA Over range settings: Voltage $\pm 8\%$, current $+8\%/-1\%$ 4 analog output -10.8 ... +10.8 V 5 analog output 0 ... +10.8 V 6 analog output 0 ... +21.6 mA 7 analog output +3.80 ... +21.6 mA
Analog Output [1-4] Minimum Value	Property	Minimum value of the output AO[1-4] as initialization value. If this value is written to the respective channel server, the minimum value is output on the module.
Analog Output [1-4] Maximum Value	Property	Maximum value of the output AO[1-4] as initialization value. If this value is written to the respective channel server, the maximum value is output on the module.
Analog Output [1-4] Set LED	Output	For setting the status LED. When an error occurs, the LED blinks. 0 HW status active 1 LED on 2 LED off

Error Bits	State	<p>In this server the error status bits are shown. The respective bits mean the following:</p> <table border="1"> <tr><td>Bit 1</td><td>Edge limitation output AO1</td></tr> <tr><td>Bit 2</td><td>Edge limitation output AO2</td></tr> <tr><td>Bit 3</td><td>Edge limitation output AO3</td></tr> <tr><td>Bit 4</td><td>Edge limitation output AO4</td></tr> <tr><td>Bit 5</td><td>Over temperature on output AO1</td></tr> <tr><td>Bit 6</td><td>Over temperature on output AO2</td></tr> <tr><td>Bit 7</td><td>Over temperature on output AO3</td></tr> <tr><td>Bit 8</td><td>Over temperature on output AO4</td></tr> <tr><td>Bit 9</td><td>Over current on output AO1</td></tr> <tr><td>Bit 10</td><td>Over current on output AO2</td></tr> <tr><td>Bit 11</td><td>Over current on output AO3</td></tr> <tr><td>Bit 12</td><td>Over current on output AO4</td></tr> <tr><td>Bit 13</td><td>Calibration data could not be read</td></tr> <tr><td>Bit 14</td><td>Calibration data invalid (CRC error)</td></tr> <tr><td>Bit 15</td><td>Supply voltage is not OK</td></tr> </table>	Bit 1	Edge limitation output AO1	Bit 2	Edge limitation output AO2	Bit 3	Edge limitation output AO3	Bit 4	Edge limitation output AO4	Bit 5	Over temperature on output AO1	Bit 6	Over temperature on output AO2	Bit 7	Over temperature on output AO3	Bit 8	Over temperature on output AO4	Bit 9	Over current on output AO1	Bit 10	Over current on output AO2	Bit 11	Over current on output AO3	Bit 12	Over current on output AO4	Bit 13	Calibration data could not be read	Bit 14	Calibration data invalid (CRC error)	Bit 15	Supply voltage is not OK
	Bit 1	Edge limitation output AO1																														
	Bit 2	Edge limitation output AO2																														
	Bit 3	Edge limitation output AO3																														
	Bit 4	Edge limitation output AO4																														
	Bit 5	Over temperature on output AO1																														
	Bit 6	Over temperature on output AO2																														
	Bit 7	Over temperature on output AO3																														
	Bit 8	Over temperature on output AO4																														
	Bit 9	Over current on output AO1																														
	Bit 10	Over current on output AO2																														
	Bit 11	Over current on output AO3																														
	Bit 12	Over current on output AO4																														
	Bit 13	Calibration data could not be read																														
	Bit 14	Calibration data invalid (CRC error)																														
Bit 15	Supply voltage is not OK																															
External Voltage OK	State	This server indicates whether the external module supply is OK.																														

7.3 Time Offset Mode

Analog Output Time Offset	Property	<p>This setting defines whether the time offset mode should be used for the analog outputs.</p> <table border="1" data-bbox="405 256 1016 323"> <tr> <td>0</td> <td>time offset mode for analog outputs deactivated</td> </tr> <tr> <td>1</td> <td>time offset mode for analog outputs activated</td> </tr> </table> <p>as initialization value</p>	0	time offset mode for analog outputs deactivated	1	time offset mode for analog outputs activated
0	time offset mode for analog outputs deactivated					
1	time offset mode for analog outputs activated					
Analog Output [1-4] Time Offset	Output	<p>Time offset for the according analog input in μs. Valid limits: 10 μs to (cycle time - 10 μs) Example: 1 ms cycle time (bus time) ... Range: 10 μs to 990 μs Return value/status value:</p> <table border="1" data-bbox="405 443 1016 507"> <tr> <td>-1</td> <td>feature is deactivated</td> </tr> <tr> <td>-2</td> <td>defined value is outside the valid limits</td> </tr> </table>	-1	feature is deactivated	-2	defined value is outside the valid limits
-1	feature is deactivated					
-2	defined value is outside the valid limits					
Time Offset Error Bits	State	<p>Bit 1 ... The defined time offsets were invalid and have been adjusted automatically. This can happen when the distance between two sample points is too small (< 8 μs).</p>				

7.4 Communication Interfaces

ALARM	Downlink	<p>With this downlink the corresponding alarm class can be placed via the hardware editor.</p>
-------	----------	--

7.5 Internal Properties

The values for the analog output are transmitted as 16-bit value in a range of $\pm 30,000$ resp. 0-60,000 increments.

To be able to use the full resolution of the DAC values for the minimum and maximum value settings are needed which allow 60,000 increments.

At ± 10 V this then is e.g. $\pm 30,000$.

If the overrange setting is used, on the analog output for the voltage ± 8 % and for the current $+8$ %/ -1 % can be set.

e.g.: Analog output Config = 4, Analog output Min/Max Value = $\pm 32,400$

Analog output 30.000 = 10 V 32,400 = 10.8 V

Analog output -30.000 = -10 V -32,400 = -10.8 V

7.5.1 Time Offset Mode for Analog Outputs

This mode can define the time of writing data to the DAC in μs in relation to the SDIAS sync for each single analog output channel during runtime. The appropriate value must simply be written to one of the servers "AO[x]TimeOffset". This is then accepted by the firmware with the next SDIAS sync.

7.5.2 Limitations of this Mode

- The configuration of the new set points (as PDO) is valid in the next cycle.
- Set points are not available during 10 μs after the sync. The hardware class does not allow any inputs less than 10 μs .
- Set points are not available during 10 μs before the sync. The hardware does not allow any inputs greater than this value.
- The difference between 2 set points must be at least 8 μs . All other inputs are moved by the module to the appropriate area.

Documentation Changes

Change date	Affected page(s)	Chapter	Note
23.06.2017	4	1.2 Analog Output Current Specifications	Values Ovrerrange changed
07.07.2017	10	3 Connector Layout	Internally connected added
26.07.2017	3	1.2 Output Accuracy Analog Outputs Voltage	Chapter new
	4	1.4 Output Accuracy Analog Outputs Current	Chapter new
	8	1.7 Voltage Monitoring External +24 V Supply	Chapter new
01.08.2017	8	1.7 Voltage Monitoring External +24 V Supply	Corrected voltage specifications
17.08.2017	8	1.9 Environmental Conditions	Added operating conditions
	12	3.2 Applicable Connectors	Added sleeve length Added info regarding ultrasonically welded strands
18.10.2017	13	3.3 Label Field	Added chapter
	17	5 Mounting	Graphic replaced
20.09.2018		3 Connector Layout	Note added
14.11.2019		7 Supported Cycle Times	Chapter added
28.02.2020	22	7 Supported Cycle Times	Text adapted
28.05.2020	22	7 Supported Cycle Times	Chapter removed
08.09.2020		7 Hardware Class AO046	Chapter added
04.11.2020	17	5 Mounting	Expansion functional ground connection